

Appln No. 10/790,993

Amdt date November 12, 2004

Reply to Office action of August 20, 2004

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Canceled)

2. (Currently Amended) ~~The A radar according to claim 1,~~
~~further~~ comprising:

a mixer that mixes a transmitted signal and a received
signal;

an A/D converter that analog-to-digital converts an output
signal of said mixer to an output data;

a processing unit that removes a dc voltage component by
subtracting a predetermined voltage value from output data of
said A/D converter, and Fourier-transforms data, which has the
dc voltage component removed therefrom by said processing unit,
so as to analyze the data; and

a bias voltage application circuit as a stage preceding
said A/D converter, wherein+ said ~~removing~~ processing unit
adopts a measurement of a voltage at a dc voltage source, which
is employed by said bias voltage application circuit, as the
removal voltage value.

3. (Original) The radar according to claim 2, wherein said
voltage measurement is obtained by measuring the voltage at said
dc voltage source with no signal received by said bias voltage
application circuit.

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4. (Original) The radar according to claim 3, further comprising a switch that discontinues conduction of a signal so as to establish a state in which no signal is received by said bias voltage application circuit.

5. (Currently Amended) ~~The A radar according to claim 1,~~
comprising:

a mixer that mixes a transmitted signal and a received signal;

an A/D converter that analog-to-digital converts an output signal of said mixer to an output data; and

a processing unit that removes a dc voltage component by subtracting a predetermined voltage value from output data of said A/D converter, and Fourier-transforms data, which has the dc voltage component removed therefrom by said processing unit, so as to analyze the data, wherein said ~~removing~~ processing unit calculates the removal voltage value on the basis of the output data of said A/D converter.

6. (Currently Amended) The radar according to claim 5, wherein said ~~removing~~ processing unit calculates the removal voltage value as an average of output data items of said A/D converter.

7. (Currently Amended) The radar according to claim 6, wherein said ~~removing~~ processing unit applies a window function to the data ~~items~~ that have the average subtracted therefrom, calculates a second average by averaging the data ~~items~~ that

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have the window function applied thereto, and subtracts the second average from the data ~~items~~ that have the window function applied thereto.

8. (Currently Amended) ~~The A radar according to claim 1,~~
comprising:

a mixer that mixes a transmitted signal and a received
signal;

an A/D converter that analog-to-digital converts an output
signal of said mixer to an output data; and

a processing unit that removes a dc voltage component by
subtracting a predetermined voltage value from output data of
said A/D converter, and Fourier-transforms data, which has the
dc voltage component removed therefrom by said processing unit,
so as to analyze the data, wherein digital filtering is
performed on the data treated by said ~~removing~~ processing unit
in order to further remove a low-frequency component.

9. (Original) The radar according to claim 8, wherein digital filtering is performed on the data that has the window function applied thereto.